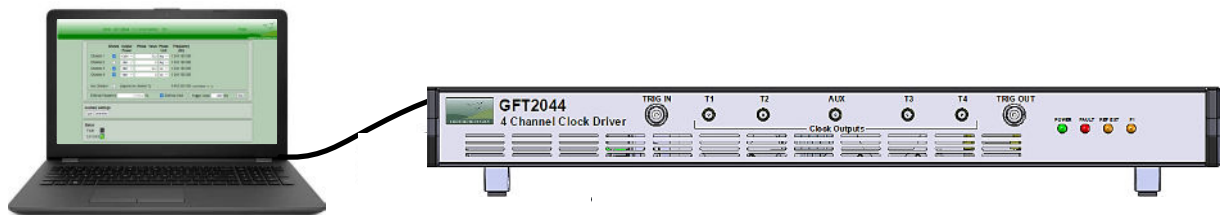


Features

- 4 synchronous high speed clock drivers
1 to 4 GHz Clock frequency
Programmable phase in step of 10 fs
-4 dBm to 5 dBm output clock level.
1 ps channel to channel jitter
- 100 MHz reference clock input
- Easy remote controlled from standard Internet browser (The user doesn't need any software)
- Compact packaging 19" W x 330 mm D x 1U H

Applications

- High Speed Digitizer system
- Arbitrary Waveform system
- RADAR
- RF Communication
- Ultrasound
- Laser Doppler Velocimetry
- ATE (Automatic Test Equipment)



Description

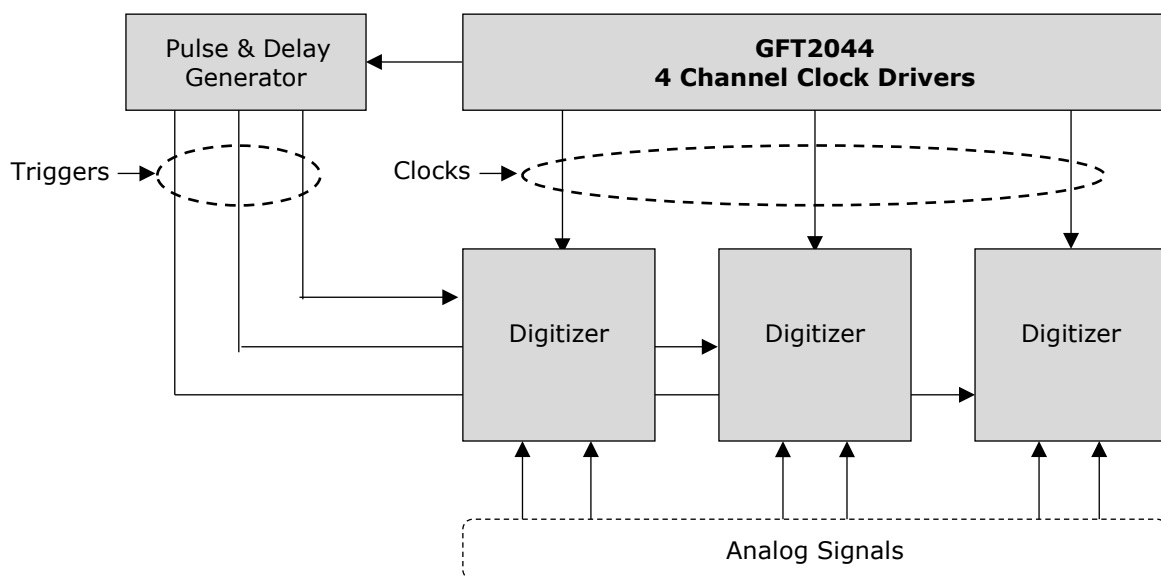
The GFT2044 is designed to provide clock distribution to synchronize up to for (4) A/D and D/A systems and arbitrary waveform generator.

Four synchronized onboard frequency synthesized generators allow to control the output clock up to 4 GHz. The synthesized generator is locked to an onboard 100 MHz reference clock and is used in conjunction with phase lock loop to control the programmed clock rate and phase value. An external 100 MHz reference can also be selected.

4 GPIO lines is available for communication to external equipment.

GFT2044 parameters may be remotely controlled over Ethernet and Internet via internal web server.

Application example: Typical application is to synchronize "Multichannel high speed digitizer system" with very low jitter.



Block diagram of the system



GFT2044

Synchronized 1 to 4 GHz Clock Drivers

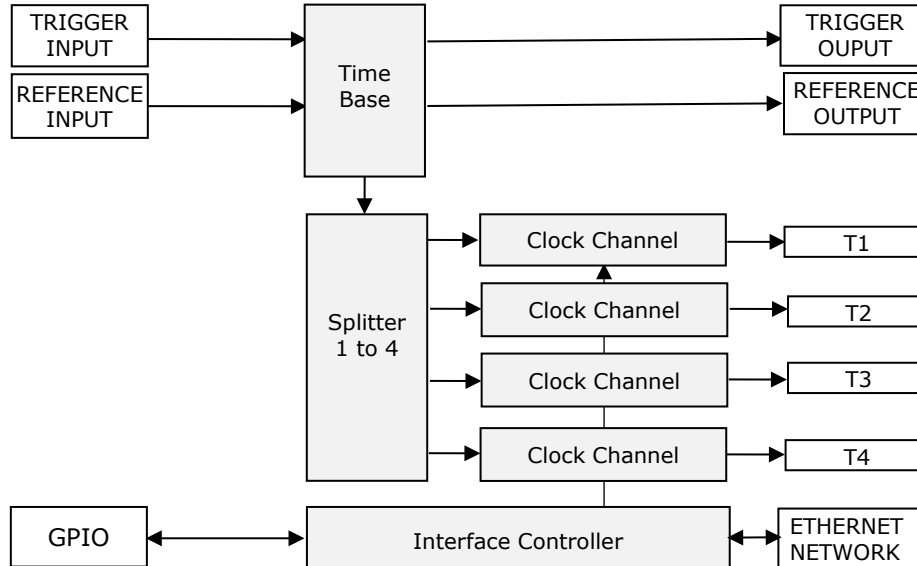
Specifications

Clock channels	
Number	4 synchronized
Frequency range	1 GHz to 4 GHz in step of 100 MHz (for other value ask to factory)
Phase range	0 to 360° in step of 10 fs
Channel to channel jitter	1 ps RMS
Internal Time Base	
Frequency	100 MHz, 100 ppb stability
Accuracy	10 ⁻⁶
Phase noise @ 1KHz offset	-155 dBc/Hz
Clock Channel Output	
Level / Shape	-4 dBm, -1 dBm, +2 dBm et +5 dBm / Square Adjustable independently on each channel
Connector	SMA
Trigger input	
Trigger Level	100 mV to 5 V
Min width	20 ns (TBC)
Trigger Output	
Function	Input trigger synchronous with reference clock
Level	10 V, Trise < 5 ns into 50 Ω
Reference Input	
Level	Min -6 dBm, typical +2 dBm, max +8 dBm, threshold 0 V, internal 50 Ω
Shape	Sine or Square
Frequency	100 MHz (for other value ask to factory)
Reference Output	
Level	400 mV p-p
Shape	Square
Frequency	100 MHz
AUX clock Output	
Function	Ask to factory for specific application
GPIO under software control	
4 Inputs or outputs	0 or 3V level, SUB-D 9 ways connector
General specifications	
Interface Control	Ethernet 10/100/1000 Mb/s
Power consumption / supply	25 W / AC 88 - 264 V, 47- 63 Hz
Operating temperature	10°C to 35° C
Weight	<5 kg
Size	19" W x 330 mm D x 1U H

Operating Information

Block diagram of the Clock driver

The 4 channels Clock driver includes the 4 following functions: A time base with 1 to 4 splitter, four programmable Clock channels and an interface controller.



Time base:

This function provides a 100 MHz low noise time base from an internal reference or an external 100 MHz reference. A trigger input allows to synchronize the clock channels from an external signal.

Clock Channel:

This function locked on time base is made with programmable phase shifter and high resolution independent programmable frequency synthesizer.

A Clock Channel output amplifier provides a clock signal adjustable on each channel from -4 to +5 dBm.

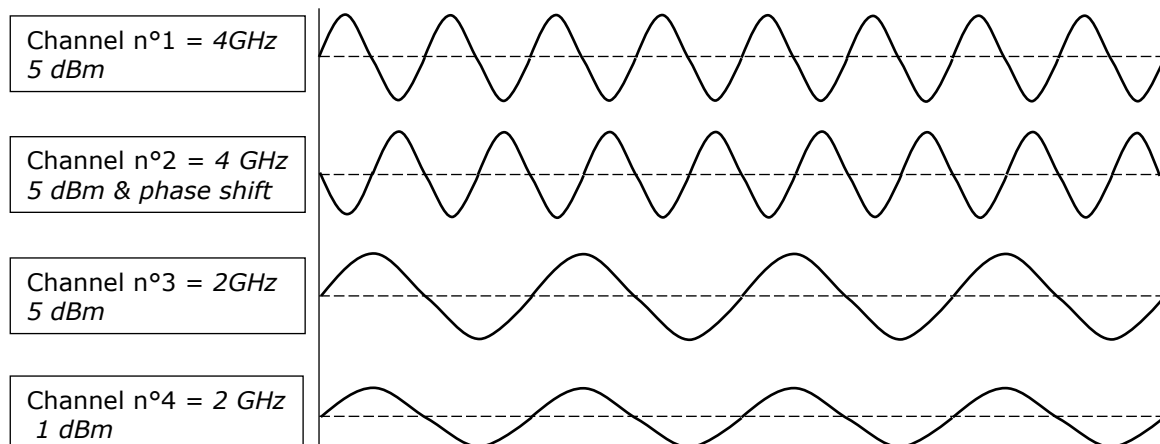
Interface Controller:

It manages internal functions and user interface. All the parameters can be remote controlled via Ethernet (10/1000 Mb/s) and Internet from internal web server.

All parameters' values are automatically saved in the unit.

GPIO lines: Four input or output lines under software command allow to control other devices at low frequency.

Example of channel output mode



Example of Clock output

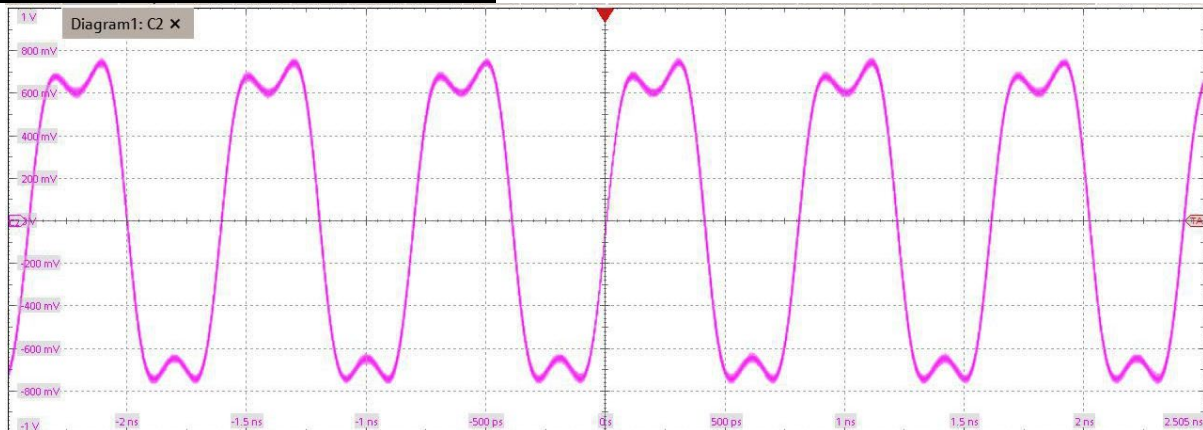
1.24416 GHz Clock into 50 Ω @ -1 dBm



Meas Group 1	Current	Max	Min	Mean	RMS	σ (S-dev)	Event count	Wave count
Amplitude	666.01 mV	727.27 mV	606.72 mV	653.6 mV	653.84 mV	17.491 mV	16188	16188
Rise time	106.25 ps	129.97 ps	93.336 ps	105.54 ps	105.66 ps	5.0591 ps	113316	16188

(100 mV/div, 500 ps/div)

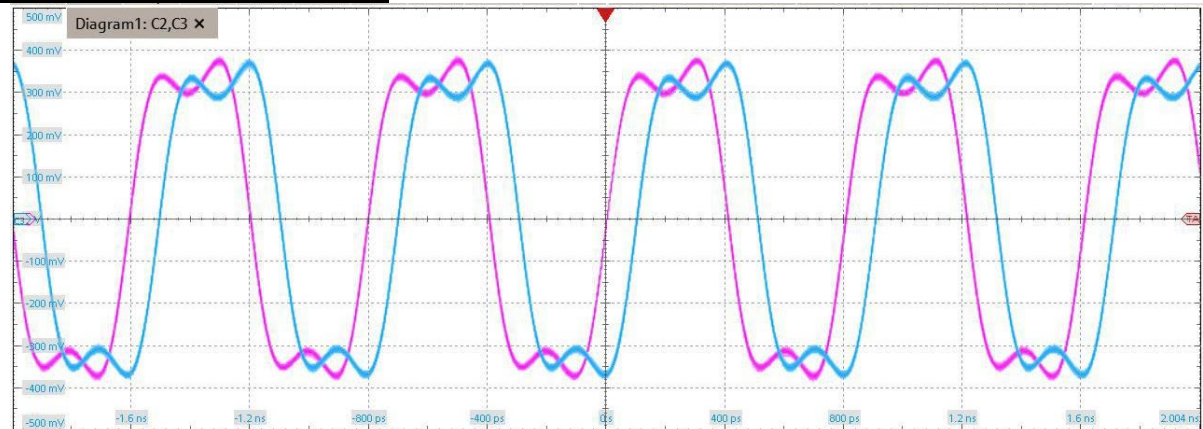
1.24416 GHz Clock into 50 Ω @ +5 dBm



Meas Group 1	Current	Max	Min	Mean	RMS	σ (S-dev)	Event count	Wave count
Amplitude	1.3399 V	1.4862 V	1.2806 V	1.3476 V	1.3478 V	23.131 mV	17704	17704
Rise time	117.76 ps	131.25 ps	99.245 ps	109.59 ps	109.64 ps	3.0601 ps	123928	17704

(200 mV/div, 500 ps/div)

T1 and T2 phased out of 100 ps



Meas Group 1	Current	Max	Min	Mean	RMS	σ (S-dev)	Event count	Wave count
Frequency	1.2455 GHz	1.2488 GHz	1.2399 GHz	1.2442 GHz	1.2442 GHz	1.1176 MHz	59772	14943
Delay	98.52 ps	109.58 ps	90.992 ps	100.09 ps	100.13 ps	2.5634 ps	14943	14943

(100 mV/div, 400 ps/div)

Control and software tools

There are two ways to control the Clock Drivers.

Easy remote way via Internet and control panel web pages.

Web page, from embedded Web server, provides easy method to configure settings.

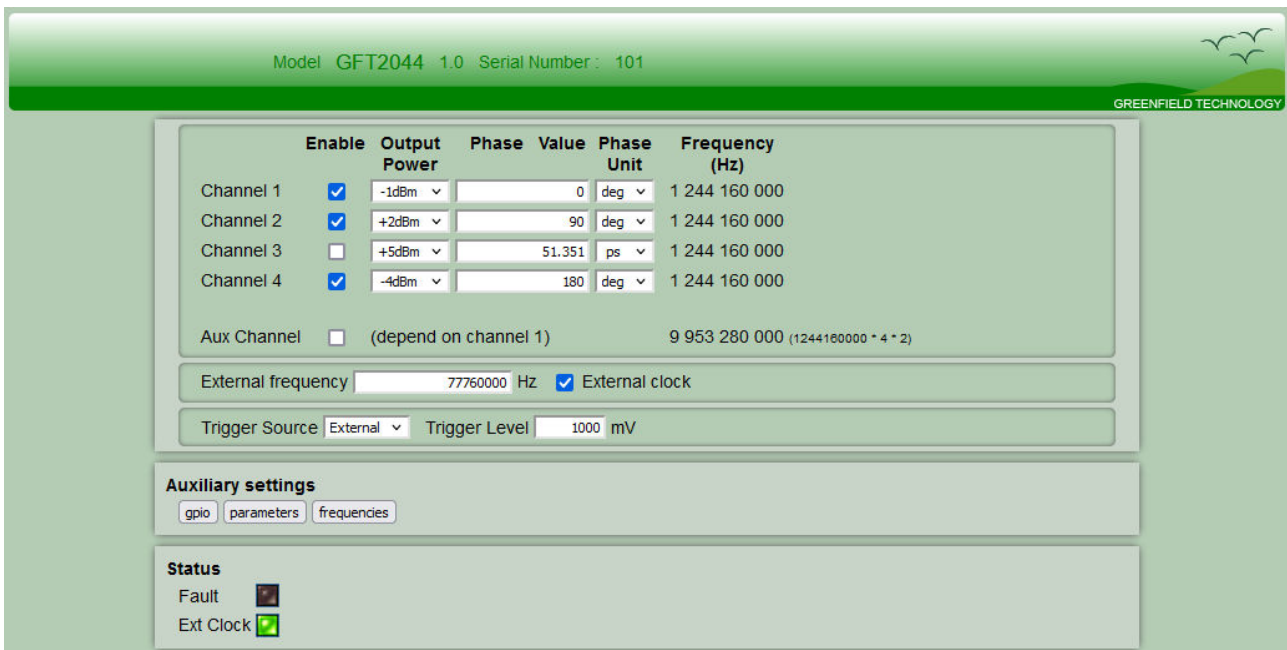
A Main menu allows to display and control

- On each channel
 - o Enable / disable clock
 - o Output power
 - o Phase value in degrees or ps
 - o Output frequency display
- External or Internal Reference
- Input trigger source (external or internal) and threshold
- Auxiliary settings
 - o GPIO
 - o Parameters (IP address, Net mask...)
 - o Output frequencies setup
- Status
 - o Fault (synthesizer lock, temperature too high)
 - o Switch to external reference indicator

The configuration information (all the settings) of the instrument is stored and saved in the GFT2044.

The web page can be opened via Internet Explorer, Mozilla Firefox or Chrome.

After connecting a cable from the GFT2044 Ethernet port to your computer network, enter the GFT2044 IP address into your PC's browser (the IP address can be identified in User's manual). The PC browser will automatically open the control panel web page on your PC.

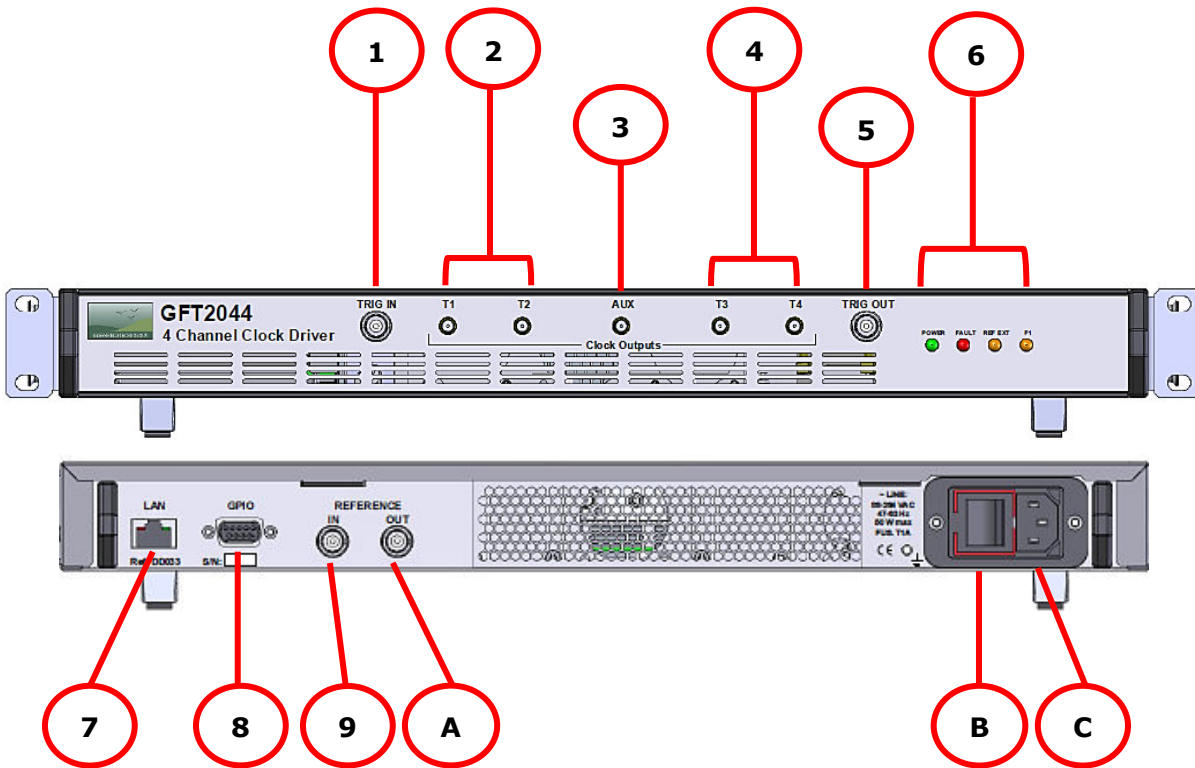


Control panel web page

General remote way via Ethernet and LabVIEW software application or other PC software application. GFT2044 offers a set of command to configure settings for each channel and trigger and control operation and status of the instrument.

Example of use is provided in a user's manual.

Front and rear panel



Connector, indicator, switch

Front panel		Rear panel	
• Connector		• Connector	
1	Trigger input: BNC connector	7	LAN connection: RJ45 connector
2	T1 & T2 clock outputs: SMA connector	8	GPIO: subD 9 ways connector
3	Auxiliary clock output: SMA connector	9	Reference input: BNC connector
4	T3 & T4 clock outputs: SMA connector	A	Reference output: BNC connector
5	Trigger output: BNC connector	B	1 A Fuse case & AC Power plug
• Indicator		• Switch	
6	Power ON, Fault, Référence externe, F1	C	Red switch for power On/off